

Notice of References Cited	Application/Control No. 10/017,179	Applicant(s)/Patent Under Reexamination CHEUNG ET AL.	
	Examiner Aaron D Matthew	Art Unit 2114	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,356,960	03-2002	Jones et al.	710/5
	B	US-4,672,534	06-1987	Kamiya, Shigeo	714/30
	C	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	United States Patent Application Publication □□ Cheung et al. □□ On-Chip Hardware Breakpoint Generator with Comprehensive Memory Operation Detection □□ Pub. No.: US 2002/0188813 A1 □□ Appl. No.: 10/128,025 □□ Pub. Date: Dec. 12, 2002
	V	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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